

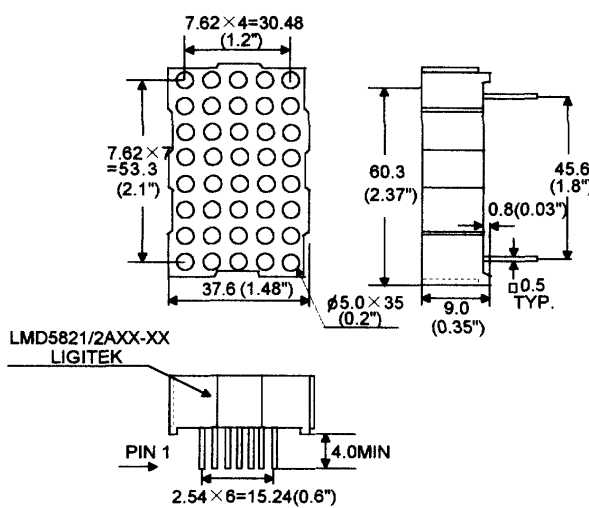
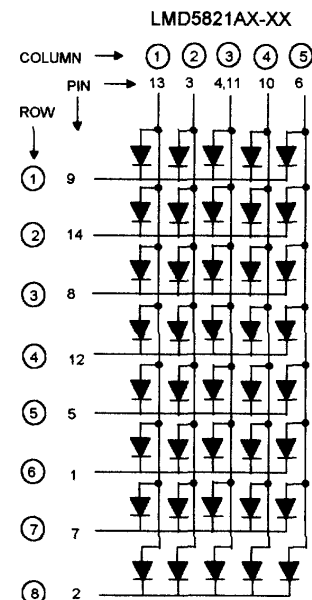
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DOT MATRIX LED DISPLAY(2.3Inch)		LMD5821AX	series	Page 1/3
PACKAGE DIMENSION		INTERNAL CIRCUIT DIAGRAM		
				
NOTE: All Dimension Are In Millimeters And (Inch) Tolerance Is $\pm 0.25(0.01")$ Unless Otherwise Noted				
• Connection To Electrical Schematic				
Electrical Connection				
PIN NO.	LMD5821AX-XX		PIN NO.	
1	Anode	Row 6		
2	Anode	Row 8		
3	Cathode	Column 2		
4	Cathode	Column 3		
5	Anode	Row 5		
6	Cathode	Column 5		
7	Anode	Row 7		
8	Anode	Row 3		
9	Anode	Row 1		
10	Cathode	Column 4		
11	Cathode	Column 3		
12	Anode	Row 4		
13	Cathode	Column 1		
14	Anode	Row 2		
文件編號: QW0905-M5821AX-XX 版本: A 生效日期: May . 22 . 1997				

• **Part Selection And Application Information (Ratings At 25°C Ambient)**

PART NO	CHIP		common cathode or anode	λ_P (nm)	$\Delta\lambda$ (nm)	Electrical					IV-M
						Vf(v)			Iv(mcd)		
	material	emitted				Min	Typ.	Max	Min	Typ.	
LMD5821ASR-XX	GaAlAs	Red	Column Cathode Row Anode	660	20	1.5	1.7	2.4	1.9	3.2	2:1
LMD5822ASR-XX	GaAlAs	Red	Column Anode Row Cathode	660	20	1.5	1.7	2.4	1.9	3.2	2:1

• **Absolute Maximum Rating (Ta=25°C)**

Parameter	Red			Green		Yellow			Orange			Unit	Remark
Forward Current Per Chip	SR												
	40										mA		
Peak Current Per Chip (Duty 1/10, 0.1MS Pulse Width)	200										mA		
Power Dissipation Per Chip	110										mW		
Derating Linear From 25°C Per Chip	0.45										mA/ °C		
Reverse Current Per Any Chip	10										μA		
Operating Temperature	-25°C TO +85°C												
Storage Temperature	-25°C TO +85°C												

Solder Temperature 1/16 Inch Below Seating Plane For 3 Seconds At 260°C

• **Test Condition For Each Parameter**

Parameter	Symbol	Unit	Test Condition
Forward Voltage Per Chip	Vf	volt	If=20mA
Luminous Intensity Per Chip	Iv	mcd	If=10mA
Peak Emission Wavelength	λ_P	nm	If=20mA
Spectral Line Half-Width	$\Delta\lambda$	nm	If=20mA
Reverse Current Any Chip	Ir	μA	Vr=5V
Luminous Intensity Matching Ratio	IV-M		